

Class	Subclass
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PATENT NUMBER

**U.S. UTILITY Patent Application**

<p>O.I.P.E.</p> <p>SCANNED <u>TRI</u> Q.A. <u>Am</u></p>	<p>PATENT DATE</p>
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JC-1118

APPLICATION NO. 09/943196	CONT/PRIOR	CLASS 438	SUBCLASS 692	ART UNIT 1765	EXAMINER Ym(z-Eronini)
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Charles May

Arrangement and method for fabricating a semiconductor wafer

PTO-2040  
12/99**ISSUING CLASSIFICATION**[illegible]

<input type="checkbox"/> <b>TERMINAL DISCLAIMER</b>	<b>DRAWINGS</b>			<b>CLAIMS ALLOWED</b>	
	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims	Print Claim for O.G.
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